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## INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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APPLICANT  
**Hiroshi KIMURA**

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## U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
		Country Code - Number - Kind Code (if known)	MM-DD-YYYY			Yes	No
		JP 2001-217657	8-10-2001				
		JP 10-335948	12-16-1998				
		JP 9-232877	9-5-1997				
		JP 03-060208	3-15-1991	Hitachi Ltd.		English Abstract	

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		NISHIMURA et al., "A 1.25Gbit/s CMOS Burst-Mode Optical Transceiver for Ethernet PON System", 2004 Symposium on VLSI Circuits Digest of Technical Papers, pp. 414-417
		YAMASHITA et al., "Novel Cell-AGC Technique for Burst-Mode CMOS Preamplifier With Wide Dynamic Range and High Sensitivity for ATM-PON System", IEEE Journal of Solid-State Circuits, Vol. 37, No. 7, July 2002, pp. 881-886

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /QZW